

RDRAM Module Code Information(1/2)

Last Updated : February 2009

<u>M</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>-</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18

1. Memory Module(M)

2. Module Configuration

D : 32 bit RIMM[®] (232pin)
 N : NexMod[™] (BGA type connector)
 P : NexMod[™] (PGA type connector)
 R : RIMM
 S : SO-RIMM[™]

3~4. Data Bit & Module Type*

16 : x16 Non-ECC 18 : x18 ECC
 73 : x72 (294pin) ECC

5. Feature

C : for Continuity Module
 R : Direct RAMBUS DRAM

6~7. Depth

04 : 4M 08 : 8M
 16 : 16M 32 : 32M
 64 : 64M
 NT : for Continuity Module

8. Refresh, # of banks, Interface, VDD, VDDQ

2 : 16K/32ms Ref, Dependent 32Bank, RSL,
 2.5V, 2.5V
 3 : 16K/32ms Ref, Dependent 16Bank, RSL,
 2.5V, 2.5V
 6 : 32K/32ms Ref, Dependent 32Bank, RSL,
 2.5V, 2.5V
 Y : CNT,CNT,CNT,CNT,CNT
 ※ CNT : for Continuity Module

9. Number of Component

1 : 1ea 2 : 2ea 3 : 3ea
 4 : 4ea 5 : 5ea 6 : 6ea
 7 : 7ea 8 : 8ea 9 : 9ea
 A : 10ea B : 11ea C : 12ea
 D : 13ea G : 16ea W : 32ea
 M : for Continuity Module

10. Component Generation

M : M-die
 A : A-die
 B : B-die
 C : C-die
 D : D-die
 E : E-die
 I : I-die

11. Package

F : WBGA
 G : WBGA (Lead-Free)
 H : WBGA (320)
 L : for Continuity Module
 M : μBGA[®] packages¹⁾ (M)²⁾
 N : μBGA[®] packages
 S : 54-μBGA[®] packages
 T : 54-WBGA (Lead-Free) R : 54-WBGA

12. PCB Revision & Type

0 : None 1 : 1st Rev.
 2 : 2nd Rev.

13. "—"

14. Power

0 : None
 C : Normal Power, Self Ref.

Notes :

- 1) μBGA[®] packages are registered trademarks of Tessera.
- 2) (M) : Mirror

RDRAM Module Code Information(2/2)

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<u>M</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	-	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>X</u>
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18

15~16. tRAC&Speed (Row Access Time)

00 : for Continuity Module

DS : DAISY CHAIN MDL

G6 : 53.3ns/ 600Mbps (300MHz)

K7 : 45ns/ 711Mbps (356MHz)

K8 : 45ns/ 800Mbps (400MHz)

M8 : 40ns/ 800Mbps (400MHz)

M9 : 35ns/ 1066Mbps (533MHz)

N1 : 32ns/ 1200Mbps (600MHz)

N9 : 32ns/ 1066Mbps (533MHz)

P3 : 31ns/ 1333Mbps (667MHz)

R6 : 27ns/ 1600Mbps (800MHz)

T9 : 32ns/ 1066Mbps (533MHz)/ tDAC 3

17~18. Customer (Special Handling)

"Customer List Reference"