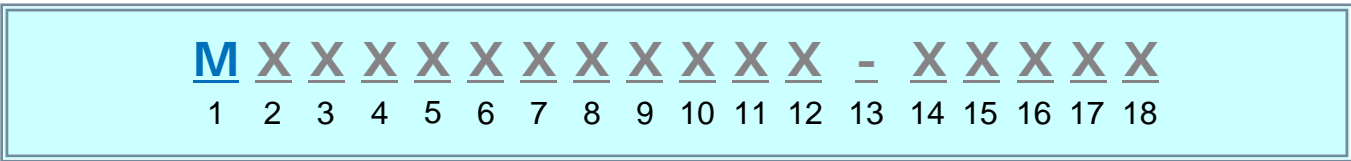


SGRAM Module Code Information



1. Memory Module(M)

2. Module Configuration

8 : 4 Byte AIMM (132pin)

3~4. Data bits

32 : x32 AIMM w/o SPD

5. Feature, Voltage

G : SGRAM, 3.3V

6~7. Depth

01 : 1M

8. # of Banks in Module

1 : 1Bank

9. Composition Component

5 : x32

10. Component Generation

11. Package

P : PQFP

12. PCB Revision & Type

0 : None

13. "—"

14. Power

C : Normal, Self Ref.

15~16. Speed

< tCC Default CL3 >

70 : 7ns

17~18. Customer (Special Handling)

"Customer List Reference"